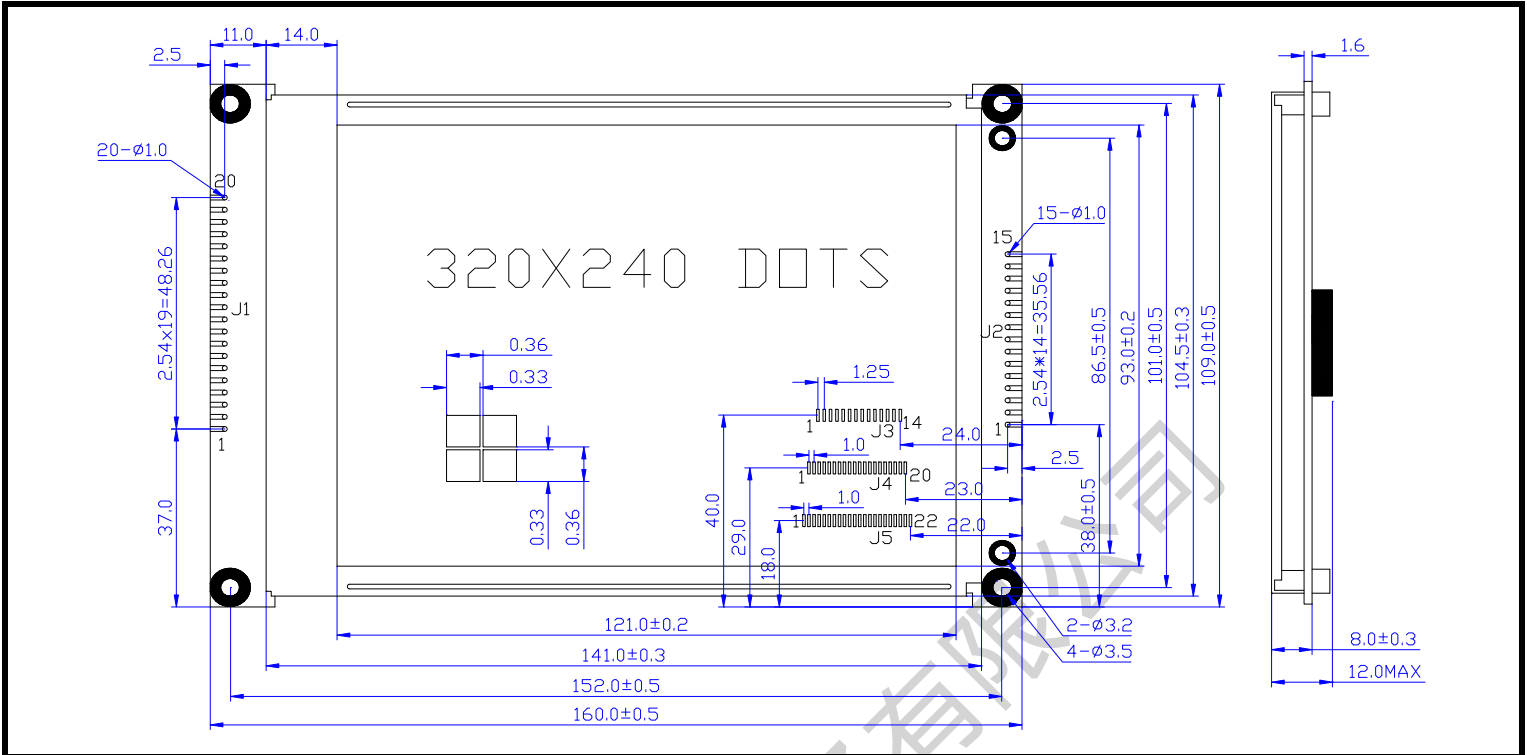


MJ320240A



②PIN CONNECTIONS(不带控制器)

Pin	J2	Function	Pin	J3
1	D0	Data Bus	1	D0
2	D1		2	D1
3	D2		3	D2
4	D3		4	D3
5	DISP	Display ON/OFF	5	DISP
6	FLM	Frame Signal	6	FLM
7	M	Alternate Signal	7	M
8	LP	Date Latch Signal	8	LP
9	CP	Date Shift Signal	9	CP
10	VDD	Power Positive(+5V)	10	VDD
11	VSS	Power Ground(0V)	11	VSS
12	VEE	Negative Voltage Output	12	V0
13	VO	Negative Voltage Adjust	13	VEE
14	FG/A	Frame Ground/BL+(+5V)	14	FG/A
15	A/K	BL+(+5V)/BL-(0V)		

③PIN CONNECTIONS (带控制器)

Pin	J1	Function	Pin	J4	Pin	J5
1	VSS	Power Ground(0V)	1	VSS	1	VSS
2	VDD	Power Positive(+5V)	2	VDD	2	VDD
3	V0	Negative Voltage Adjust	3	V0	3	V0
4	WR	Write Signal	4	WR	4	A0
5	RD	Read Signal	5	RD	5	WR
6	CS	Chip Select	6	CS	6	RD
7	A0	H:Data; L:Instruction Code	7	A0	7	DB0
8	RST	Reset Signal	8	RST		
9	DB0	Data Bus	9	DB0	14	DB7
					15	CS
16	DB7			16	DB7	16
17	NC/A	No Connect/BL+	17	VEE	17	VEE
18	VEE	Negative Voltage Output	18	SEL1	18	SEL1
19	A	BackLight+ (+5V)	19	A	19	A
20	K	BackLight- (0V)	20	K	20	K
					21	NC
					22	NC

④MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size(W×H×T)	160.0×109.0×12.0	mm
Viewing Area(W×H)	121.0×93.0	mm
Dot Pitch(W×H)	0.36×0.36	mm
Dot Size(W×H)	0.33×0.33	mm

⑤ABSOLUTE MAXIMUM RATINGS(25°C)

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage Logic	VDD	0	5.5	V
Supply Voltage Driver	VDD-VEE	0	23	V
Input Voltage	VIN	0	VDD	V